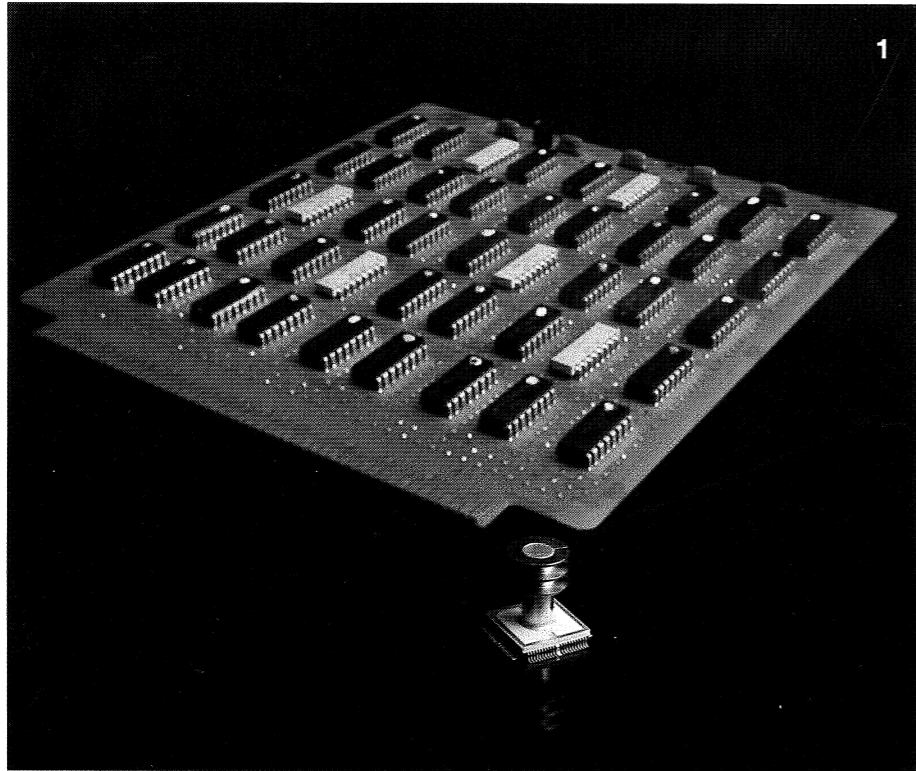
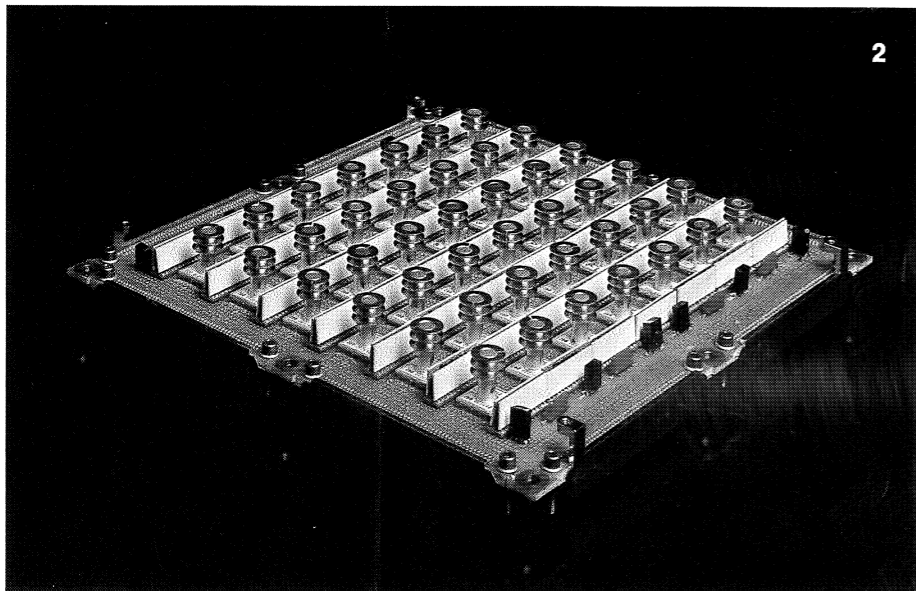


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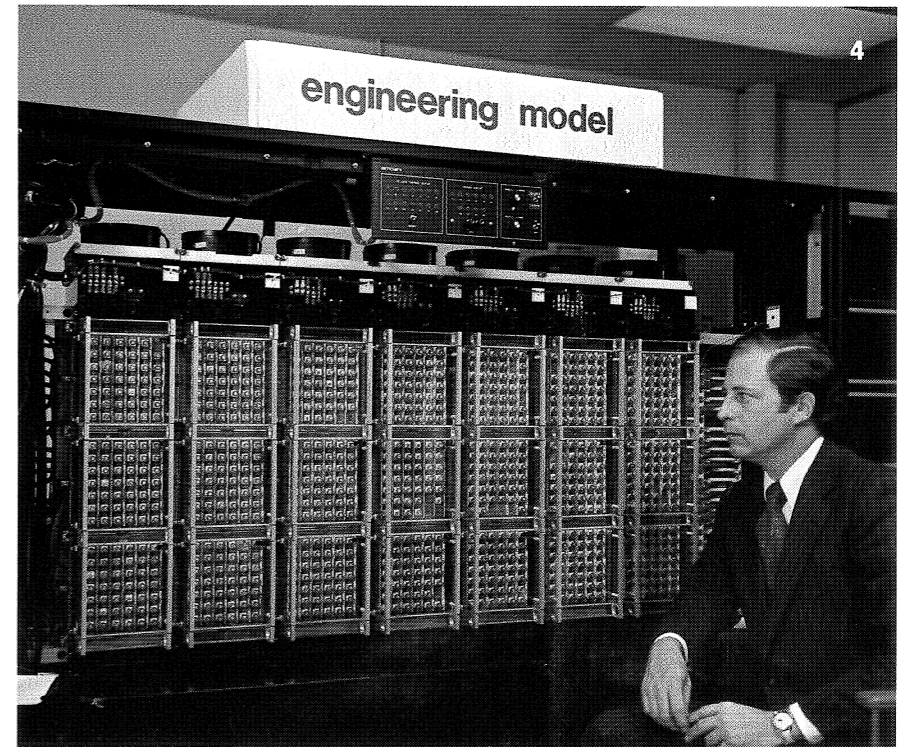
1. Amdahl "fourth generation" large scale integration (LSI) technology for large scale computer systems vs present "third generation" technology for such systems. The LSI package in the foreground contains the electronic equivalent in logic circuitry to the "third generation" integrated circuits on the printed circuit card in the background.



2. Individual LSI chip packages are mounted upon a multi-chip carrier card (MCC). The 42 elements shown replace the same number of "third generation" printed circuit cards used in existing commercially available large scale computer systems. This MCC card measures approximately 7½ inches on a side.



3. Using "third generation" technology, Amdahl engineers built this simulator of the Company's planned central processing unit (CPU) in order to test the basic design of the planned system. This simulator consists of seven bays of logic electronics and the required interconnections, and measures approximately 23 feet long, 6 feet tall and 2 feet deep.



4. Using "fourth generation" technology, the Company built this engineering model of the Company's planned CPU which should be contrasted with the "third generation" simulator shown in photograph number three. The electronics in this smaller unit contain all of the circuits present in the seven-bay simulator and are designed to perform the identical functions. The CPU shown here is approximately 6 feet long, 5 feet tall and 2 feet deep.